

Michael D. Newell

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Objective	To apply my knowledge of Computer Engineering principles through co-op employment. Available for employment starting June 2018.	
Experience	FluxData Inc.	January 2017 to August 2017 Systems Engineer Intern, updated and tested systems software in C++, C#, and Javascript, across multiple platforms
	Blackbox Biometric Inc.	June 2016 to September 2016 Software Engineering Intern, Created and performed quality test cases, updated in-house Python tools
	SRC Inc.	February 2016 to May 2016 Digital Engineering Intern, utilized Xilinx Vivado tools and FPGAs and Python programming
	RIT ITS	September 2014 to December 2015 IT Service Desk Representative, Tech support over phone, over email, and in person
	Github Projects	github.com/mikex5 Open source Guitar Hero game in C++ Automated moderator using Selenium Web Driver
Education	Rochester Institute of Technology, Bachelors of Science, Computer Engineering GPA: 3.43 Dean's List 2013, 2014, 2015, 2016, 2017 Graduating May 2018	August 2013 to present
	Interface and Digital Electronics Lab Work Interfacing between analog and digital devices, construct and program an NXP cup car.	August 2016
	Digital Systems Design II Lab Work Designing and simulating logic circuits and components and programming to FPGAs.	January 2015
	Assembly Language Lab Work Lab covering computer organization and I/O techniques of microprocessors.	August 2014
	Electronics I Lab Work Lab exploring advanced circuit components such as diodes, MOSFETs, and CMOSs.	January 2015
Activities	Engineering House Computer Committee Chairman Boy Scouts (Eagle Scout) National Honor Society	SEMI High Tech U Graduate American MENSA Mathematics Honor Society
Related Course Work	Digital Signal Processing Computer Architecture Digital IC Design Computer Organization Assembly Language Programming	Interface and Digital Electronics Applied Programming Data & Communication Networks Digital Systems Design I&II Circuit Analysis I&II

